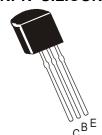


Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company



NPN SILICON HIGH SPEED SWITHCHING TRANSISTOR



P2N2369A

TO - 92 Plastic Package

LOW POWER AND HIGH SPEED SWITCHING APPLICATIONS

ABSOLUTE MAXIMUM RATINGS (T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector Emitter Voltage	V _{CEO}	15	V
Collector Base Voltage	V _{CBO}	40	V
Collector Emitter Voltage (V _{BE} =0)	V _{CES}	40	V
Emitter Base Voltage	V_{EBO}	4.5	V
Collector Current Peak	I _{CM}	500	mA
Power Dissipation @ Ta=25°C	P _D	625	mW
Operating And Storage Junction Temperature Range	T _j , T _{stg}	-65 to +200	°C

THERMAL RESISTANCE

Junction to Ambient in free air	$R_{th(j-a)}$	200	°C/W	l
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ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	VAL	UNIT	
DESCRIPTION	STWIBOL	1E31 CONDITION	MIN	MAX	UNIT
Collector Emitter Breakdown Voltage	BV _{CEO (sus)} *	I _C =10mA, I _B =0	15		V
Collector Emitter Breakdown Voltage	BV _{CES}	$I_{C}=10\mu A, V_{BE}=0$	40		V
Collector Base Breakdown Voltage	BV _{CBO}	$I_{C}=10\mu A, I_{E}=0$	40		V
Emitter Base Breakdown Voltage	BV _{EBO}	$I_E=10\mu A, I_C=0$	4.5		V
Collector Cutoff Current	I _{CBO}	V _{CB} =20V, I _E =0 Ta=150°C		30	μΑ
Collector Cutoff Current	I _{CES}	$V_{CE}=20V, V_{BE}=0$		0.4	μΑ
Base Current	I _B	V_{CE} =20V, V_{BE} =0		0.4	μΑ
Collector Emitter Saturation Voltage	V _{CE(sat)} *	I _C =10mA, I _B =1mA		0.20	V
		I _C =30mA, I _B =3mA		0.25	V
		I _C =100mA, I _B =10mA		0.5	V
		I _C =10mA, I _B =1mA		0.3	V
		Ta=125°C			



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ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	VAL	VALUE		
DESCRIPTION	STIVIBUL	TEST CONDITION	MIN	MAX	UNIT	
Base Emitter Saturation Voltage	V _{BE(sat)} *	I _C =10mA, I _B =1mA	0.7	0.85	V	
	, ,	I _C =30mA, I _B =3mA		0.15	V	
		I _C =100mA, I _B =10mA		1.60	V	
		I _C =10mA, I _B =1mA	0.59		V	
		Ta=+125°C			V	
		I _C =10mA, I _B =1mA		1.02	V	
		Ta= -55°C		1.02	V	
DC Current Gain	h _{FE} *	I _C =10mA, V _{CE} =1V	40	120		
		I _C =10mA, V _{CE} =1V	20			
		Ta= -55°C	20			
		I _C =30mA, V _{CE} =0.4V	30			
		I _C =100mA, V _{CE} =1V	20			
		I _C =10mA, V _{CE} =0.35v	40	120		

DYNAMIC CHARACTERISTICS

Output Capacitance	C _{ob}	I _E =0, V _{CB} =5V f=140KHz		4	pF
Transition Frequency	f _T	V _{CE} =10V,I _C =10mA f=100MH _z	500		MHz

SWITCHING CHARCTERISTICS

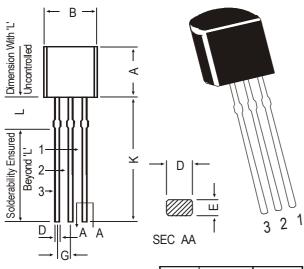
Turn on Time		$I_{C}=10$ mA, $I_{B1}=3$ mA, $I_{B2}=1.5$ mA, $V_{CC}=3$ V	12	ns
Turn off Time	t _{off}	I _C =10mA, I _{B1} =3mA, V _{CC} =3V,I _{B2} =1.5mA	15	ns
Storage Time		I _C =100mA, I _{B1} =10mA, I _{B2} = 10mA, V _{CC} =10V	13	ns

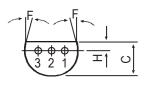
*Pulse Condition: Width ≤ 300ms, Duty Cycle ≤ 2%.

TO - 92 **Plastic Package**

TO-92 Plastic Package

TO-92 Transistors in Tape and Ammo Pack



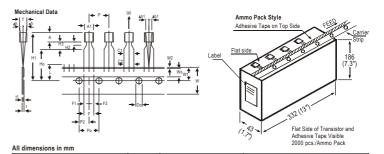


PIN CONFIGURATION

- 1. EMITTER
- 2. BASE
- 3. COLLECTOR

S	EC AA	<u> </u>	3 2 1
	DIM	MIN.	MAX.
	Α	4.32	5.33
	В	4.45	5.20
	С	3.18	4.19
	D	0.41	0.55
	Е	0.35	0.50
	F	5 D	EG
	G	1.14	1.40
	Н	1.14	1.53
	K	12.70	_
	L	1.982	2.082
	All dimir	eione in m	

All diminsions in mm.



		SPECIFICATION				
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS
BODY WIDTH	A1	4.0		4.8		
BODY HEIGHT	A	4.8		5.2		
BODY THICKNESS	T	3.9		4.2		
PITCH OF COMPONENT	P		12.7		± 1.0	
FEED HOLE PITCH	Po		12.7		± 0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH
FEED HOLE CENTRE TO						
COMPONENT CENTRE	P2		6.35		± 0.4	TO BE MEASURED AT BOTTOM OF CLINCH
DISTANCE BETWEEN OUTER					+ 0.6	
LEADS	F		5.08		-0.0	
COMPONENT ALIGNMENT SIDE VIEW	Δh		0	1.0		AT TOP OF BODY
COMPONENT ALIGNMENT FRONT VIEW	∆h1		0	1.3		AT TOP OF BODY
TAPE WIDTH	W		18		± 0.5	
HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2	
HOLE POSITION	W1		9		+ 0.7	
					- 0.5	
HOLD-DOWN TAPE POSITION	W2		0.5		± 0.2	
LEAD WIRE CLINCH HEIGHT	Ho		16		± 0.5	
COMPONENT HEIGHT	H1			23.25		
LENGTH OF SNIPPED LEADS	L			11.0		

1.2

1.45 3.0

t1 0.3-0.6

FEED HOLE DIAMETER

TOTAL TAPE THICKNESS LEAD - TO - LEAD DISTANCE

STAND OFF CLINCH HEIGHT

LEAD PARALLELISM PULL - OUT FORCE

- NOTES

 1. Maximum alignment deviation between leads will not to be greater than 0.2mm.

 2. Maximum non-cumulative variation between lape fleed holies shall not exceed 1 mm in 20 pitches.

 3. Holddown lape will not exceed beyond the edge(s) of carrier tape and there shall be no exposure of adhesive.

 4. There will be no more than three (3) consecutive missing components in a tape.

 5. A tape traitier, having at least three fleed holes are provided after the last component in a tape.

 6. Splices should not interfere with the sprocket feed holes.

Do

F1, F2

H2 H3 0.45

| C1 - C2 |

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX				
	Details Net Weight / Qty		Size	Qty	Size	Qty	Gr Wt		
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs		
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs		

Notes P2N2369A

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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